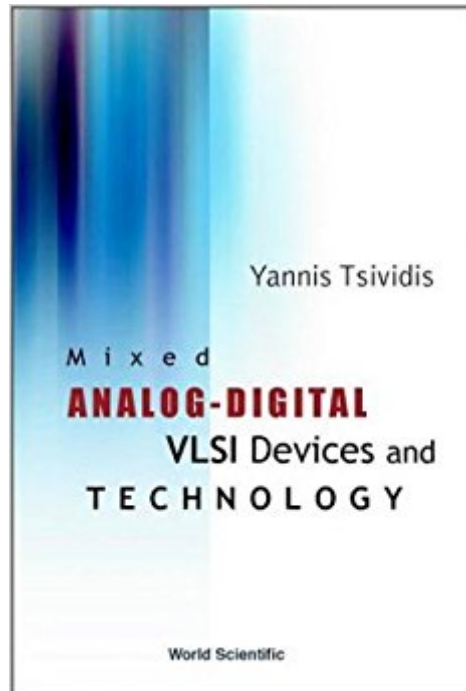




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Mixed Analog-Digital Vlsi Device And Technology



Synopsis

Improve your circuit-design potential with this expert guide to the devices and technology used in mixed analog digital VLSI chips for such high-volume applications as hard-disk drives, wireless telephones, and consumer electronics. The book provides you with a critical understanding of device models, fabrication technology, and layout as they apply to mixed analog digital circuits. You will learn about the many device-modeling requirements for analog work, as well as the pitfalls in models used today for computer simulators such as Spice. Also included is information on fabrication technologies developed specifically for mixed-signal VLSI chips, plus guidance on the layout of mixed analog digital chips for a high degree of analog-device matching and minimum digital-to-analog interference. This reference book features an intuitive introduction to MOSFET operation that will enable you to view with insight any MOSFET model besides thorough discussions on valuable large-signal and small-signal models. Filled with practical information, this first-of-its-kind book will help you grasp the nuances of mixed-signal VLSI-device models and layout that are crucial to the design of high-performance chips.

Book Information

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Customer Reviews

Yannis Tsividis is Charles Batchelor Professor at Columbia University. He has worked at AT&T Bell Laboratories and Motorola Semiconductor, and has consulted on mixed analog-digital chips for a variety of companies. He is the author of Operation and Modeling of the MOS Transistor, published

with McGraw-Hill (2nd edition, 1999).

The book can be used as a quick review of CMOS VLSI in order to step forward in analog design as well as for novice that want to have a very good introduction. It helped me quit a lot. Simple language (as simple as it can be in VLSI) and "user-friendly" writing.

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